

IN THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) A Newcard device to electrically couple a first and second subsystem of a computer, the computer being partitioned into the first and second subsystems based on at least one predefined criteria, the Newcard device comprising:
 - a first port electrically coupled to the first subsystem by a first connector;
 - a second port electrically coupled to the second subsystem by a second connector, wherein the second port includes at least one high speed serial communications bus, the second subsystem including a user console having a power control providing a status signal to the first subsystem via the Newcard device; and
 - a communication component electrically coupled to the first and second ports, wherein the communication component is operable to control signals transferred between the first and second connectors; and
 - an optional power adapter connected to provide power to the second subsystem including the user console, whereby the optional power adapter is operable to receive an AC power input to be converted to various voltages as required by the user console.
2. (Original) The device of claim 1, wherein the at least one high speed serial communications bus conforms to PCIE standard.
3. (Original) The device of claim 1, wherein the second port includes a second serial communications bus conforming to USB standard.
4. (Original) The device of claim 1, wherein the first connector includes 28 pins.
5. (Original) The device of claim 4, wherein at least one of the 28 pins is used to transfer signals conforming to PCIE standard.

6. (Original) The device of claim 1, wherein the first and second subsystems are coupled by two Newcard devices connected in parallel, wherein the two Newcard devices are substantially identical.
7. (Original) The device of claim 1, wherein the second subsystem is defined to include components operable to interact with a user.
8. (Original) The device of claim 1, wherein a first predefined criteria is heat generation and a second predefined criteria is noise generation.
9. (Original) The device of claim 8, wherein the first subsystem is placed at a sufficient distance away from a user to substantially reduce effects of the heat generation and the noise generation.
10. (Original) The device of claim 8, wherein the first subsystem includes a processor and a fan assembly included in the computer, wherein an operation of the processor and the fan assembly causes the heat generation and the noise generation.
11. (Currently Amended) A method for partitioning a computer into subsystems, the method comprising:
 - preparing a first subsystem, wherein the first subsystem is defined to include certain selectable components of the computer having at least one common property;
 - preparing a second subsystem, wherein the second subsystem is defined to include remaining components of the computer; and
 - electrically coupling the first and second subsystems by at least one Newcard device, wherein the at least one Newcard device includes:
 - a first port electrically coupled to the first subsystem by a first connector;
 - a second port electrically coupled to the second subsystem by a second connector, wherein the second port includes at least one high speed serial communications bus, the second subsystem including a user console having a power control providing a status signal to the first subsystem via the Newcard device; and

a communication component electrically coupled to the first and second ports, wherein the communication component is operable to control signals transferred between the first and second connector; and

providing an optional power adapter connected to provide power to the second subsystem including the user console, whereby the optional power adapter is operable to receive an AC power input to be converted to various voltages as required by the user console.

12. (Original) The method of claim 11, wherein the at least one high speed serial communications bus conforms to PCIE standard.
13. (Original) The method of claim 11, wherein the second port includes a second serial communications bus conforming to USB standard.
14. (Original) The method of claim 11, wherein the first connector includes 28 pins.
15. (Original) The method of claim 14, wherein at least one of the 28 pins is used to transfer signals conforming to PCIE standard.
16. (Original) The method of claim 11, wherein the first and second subsystems are coupled by two Newcards connected in parallel, wherein the two Newcards are substantially identical.
17. (Original) The method of claim 11, wherein the second subsystem is defined to include components operable to interact with a user.
18. (Original) The method of claim 11, wherein a first common property is heat generation and a second common property is noise generation.
19. (Original) The method of claim 18, wherein the first subsystem is placed at a sufficient distance away from a user to substantially reduce effects of the heat generation and the noise generation.

20. (Original) The method of claim 18, wherein the certain selectable components include a processor and a fan assembly, wherein an operation of the processor and the fan assembly causes the heat generation and the noise generation.
21. (Currently Amended) An information handling system comprising:
a first subsystem including a processor and a memory coupled to the processor;
a second subsystem including at least one expansion card; and
a Newcard device electrically coupled to the first and second subsystems,
wherein the Newcard device includes:
a first port electrically coupled to the first subsystem by a first connector;
a second port electrically coupled to the second subsystem by a second connector, wherein the second port includes at least one high speed serial communications bus, the second subsystem including a user console having a power control providing a status signal to the first subsystem via the Newcard device; and
a communication component electrically coupled to the first and second ports, wherein the communication component is directed by the processor to control signal transfer between the first and second connectors; and
an optional power adapter connected to provide power to the second subsystem including the user console, whereby the optional power adapter is operable to receive an AC power input to be converted to various voltages as required by the user console.
22. (Original) The system of claim 21, wherein the at least one expansion card is operable to receive data from the processor via the at least one high speed serial communications bus.